

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number : 10/595,303 Confirmation No.: 7114
Applicant : Roy KNECHTEL
Filed : May 19, 2006
Title : METHOD AND DEVICE FOR SECURE, INSULATED AND
ELECTRICALLY CONDUCTIVE ASSEMBLING OF TREATED
SEMICONDUCTOR WAFERS
TC/Art Unit : 2823
Examiner: : PARENDO, Kevin
Docket No. : 60291.000048
Customer No. : **21967**

MAIL STOP RCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT/RESPONSE

Sir,

Responsive to the Office Action ("Office Action") dated January 14, 2011, Applicant respectfully requests favorable reconsideration of the above-identified patent application in view of the following amendments/remarks, which are believed to place the above-identified patent application in condition for allowance.

Amendments to the claims begin on page 2 of this Paper.

Remarks/Arguments begin on page 8 of this Paper.